

S9189		USPT	"level shifter" with voltage near increas\$ near "clock signal"	<input type="checkbox"/>
S9188		USPT	"level shifter" with voltage near clock and amplitude and driv\$ near "flip flops"	<input type="checkbox"/>
S9187		USPT	6232949.pn. and "level shifter" with voltage with clock with driv\$ and "flip flops"	<input type="checkbox"/>
S9186		USPT	"level shifter" with voltage with clock with driv\$ and "flip flops" and display\$	<input type="checkbox"/>
S9185		USPT	"shift register" with "flip flops" with synch\$ with "clock signal" and level with shift with voltage with clock	<input type="checkbox"/>
S9184		USPT	"shift register" with "flip flop" with synch\$ near "clock signal" and display\$ and matrix with pixels and scan\$ and data	<input type="checkbox"/>
S9183		USPT	("shift register" with "flip flop" with synch\$ near "clock signal" and display\$ and matrix with pixels and scan\$ and data) and increas\$ with amplitude	<input type="checkbox"/>
S9182		USPT	("shift register" with "flip flop" with synch\$ near "clock signal" and display\$ and matrix with pixels and scan\$ and data) and increas\$ with voltage with amplitude	<input type="checkbox"/>
S9181		USPT	("shift register" with "flip flop" with synch\$ near "clock signal" and display\$ and matrix with pixels and scan\$ and data) and shift\$	<input type="checkbox"/>
S9180		USPT	("shift register" with "flip flop" with synch\$ near "clock signal" and display\$ and matrix with pixels and scan\$ and data) and level with clock with driv\$	<input type="checkbox"/>
S9179		USPT	("shift register" with "flip flop" with synch\$ near "clock signal" and display\$ and matrix with pixels and scan\$ and data) and shift\$ with clock with driv\$	<input type="checkbox"/>
			("shift register" with "flip flop" with	<input type="checkbox"/>

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S9178	<input type="text"/>	USPT	synch\$ near "clock signal" and display\$ and matrix with pixels and scan\$ and data) and shift\$	<input type="checkbox"/>
S9177	<input type="text"/>	USPT	("shift register" with "flip flop" with synch\$ near "clock signal" and display\$ and matrix with pixels and scan\$ and data) and shift\$ near level\$	<input type="checkbox"/>
S9176	<input type="text"/>	USPT	("shift register" with "flip flop" with synch\$ near "clock signal" and display\$ and matrix with pixels and scan\$ and data) and "level shifter"	<input type="checkbox"/>
S9175	<input type="text"/>	USPT	"shift register" with "flip flop" with synch\$ near "clock signal" and display\$ and matrix with pixels and scan\$ and data	<input type="checkbox"/>
S9174	<input type="text"/>	USPT	"shift register" with "flip flop" with synch\$ near "clock signal" and display\$ and matrix with pixels and scan\$ with lin\$ with data	<input type="checkbox"/>
S9173	<input type="text"/>	USPT	"shift register" with "flip flop" with synch\$ near "clock signal" and "level shifter"	<input type="checkbox"/>

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